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Fig. 1

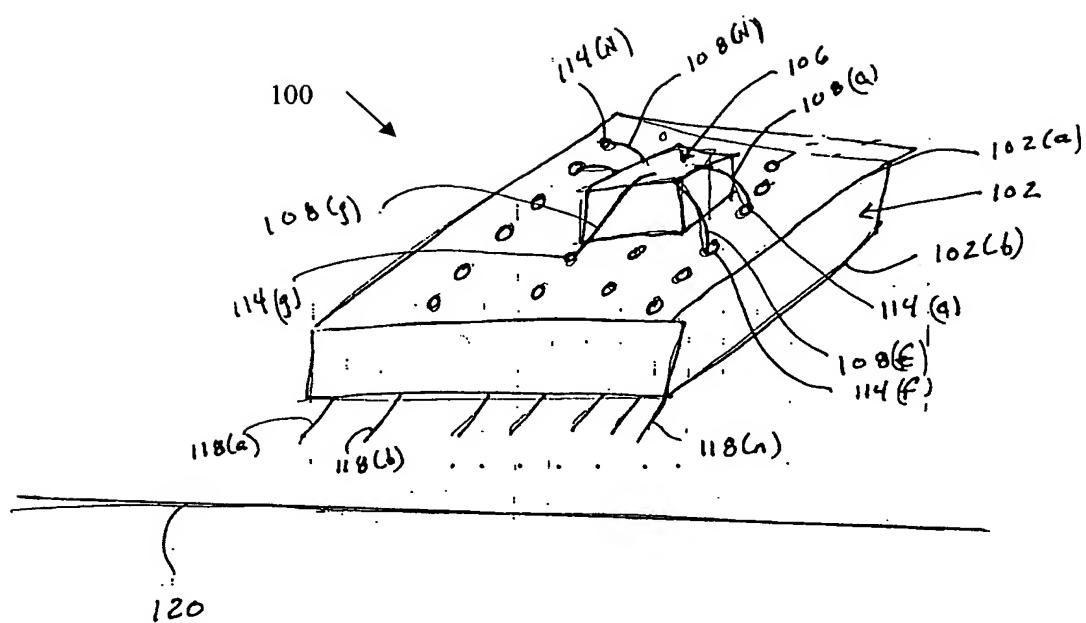


Fig. 2

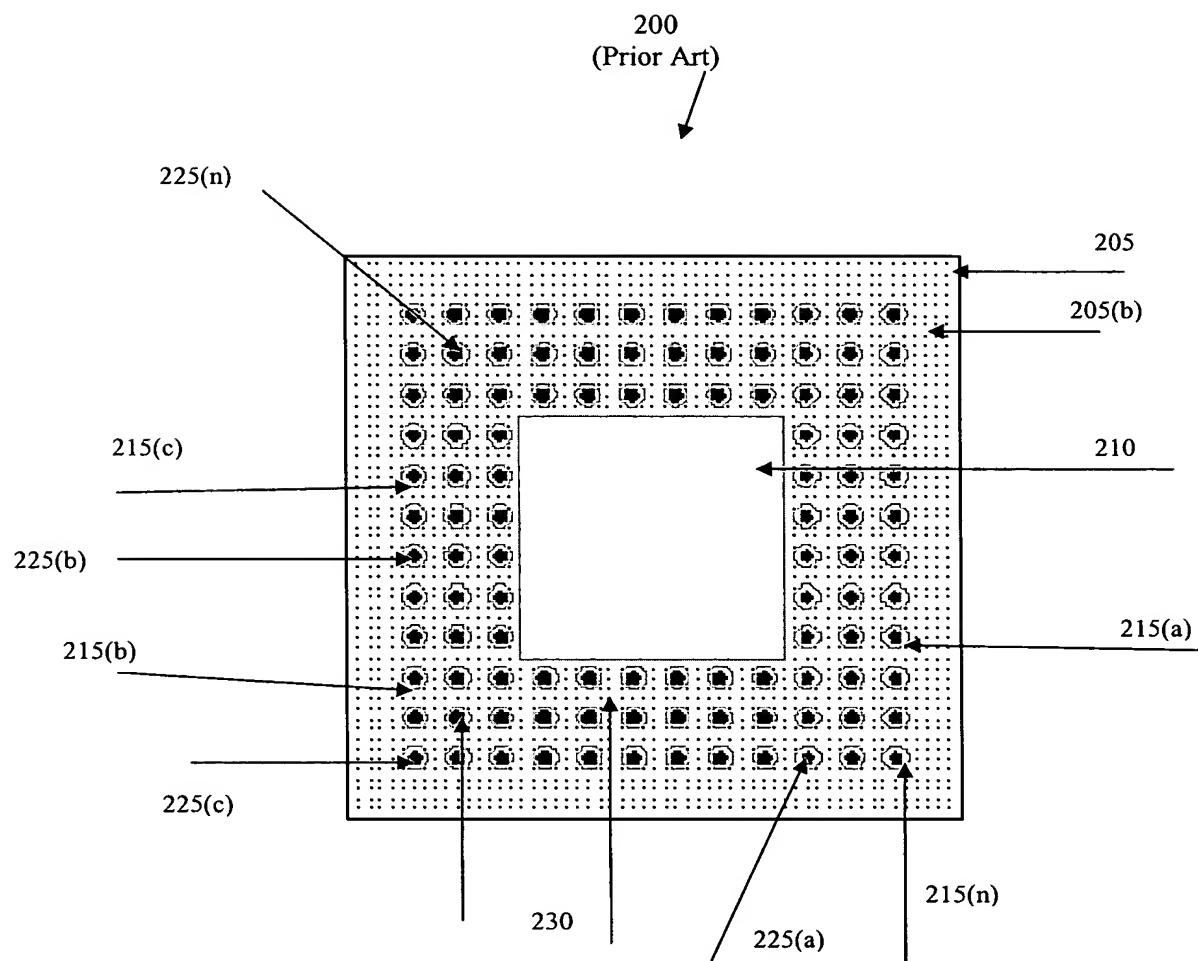


Fig. 3

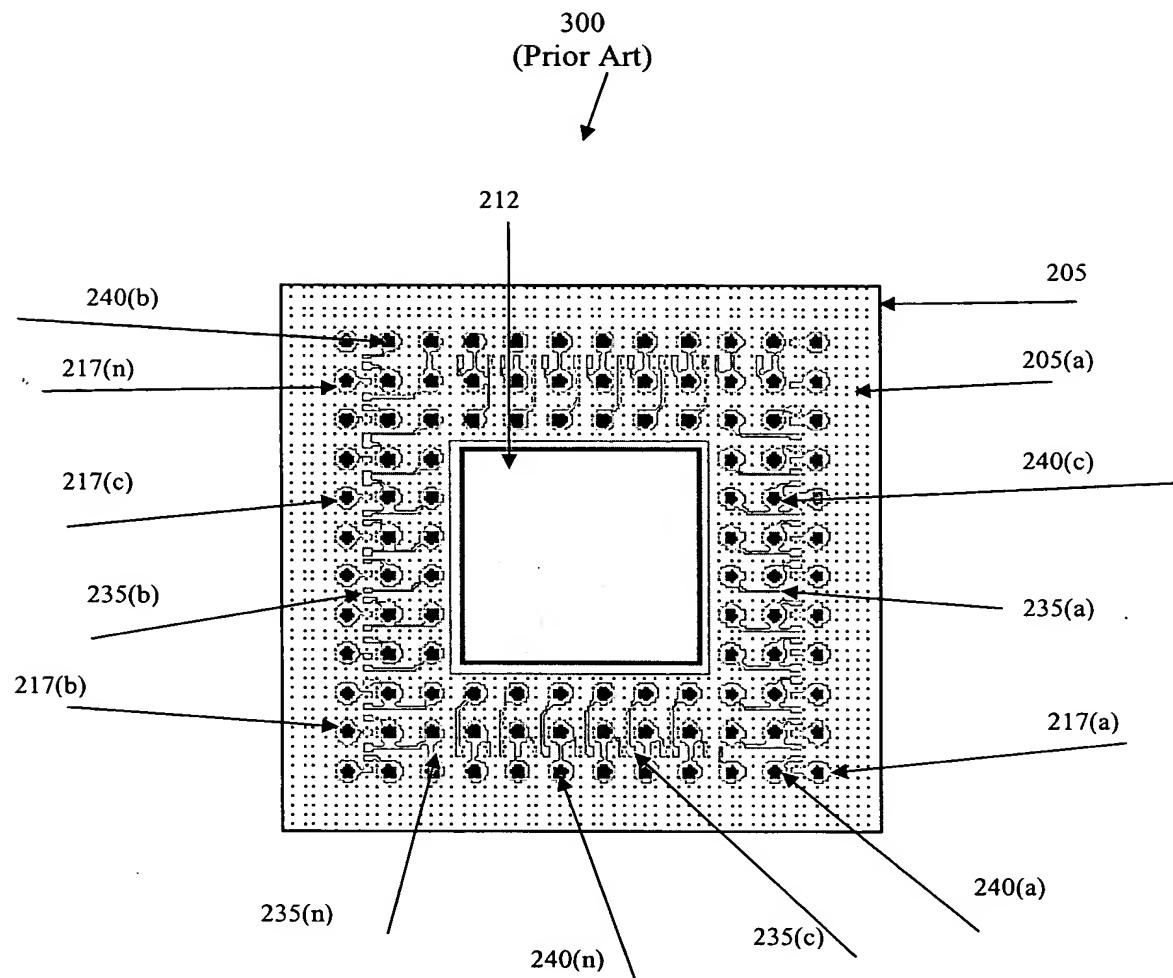


Fig. 4

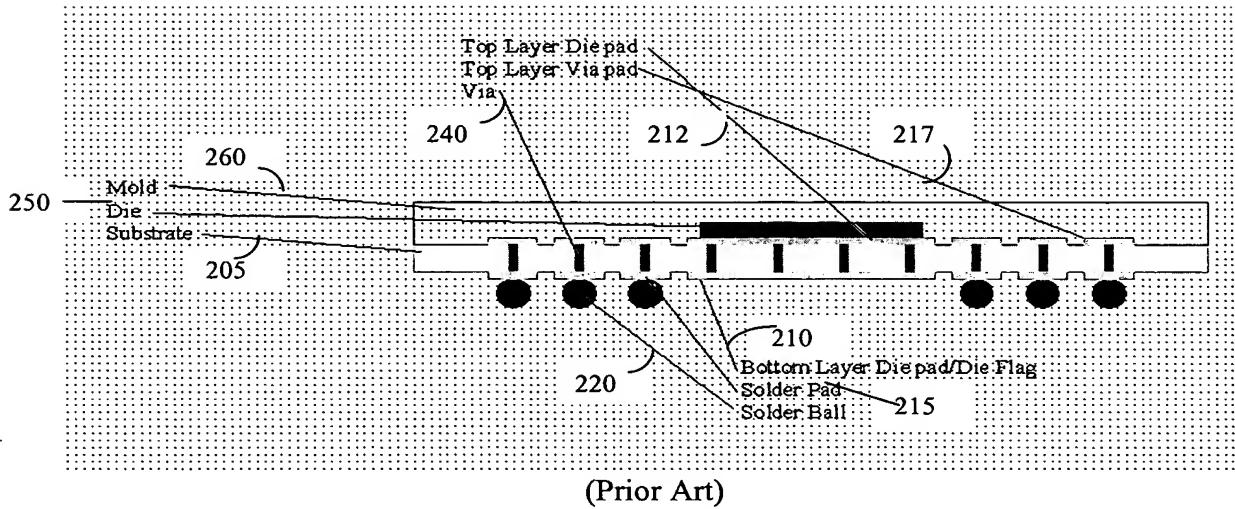


Fig. 5A

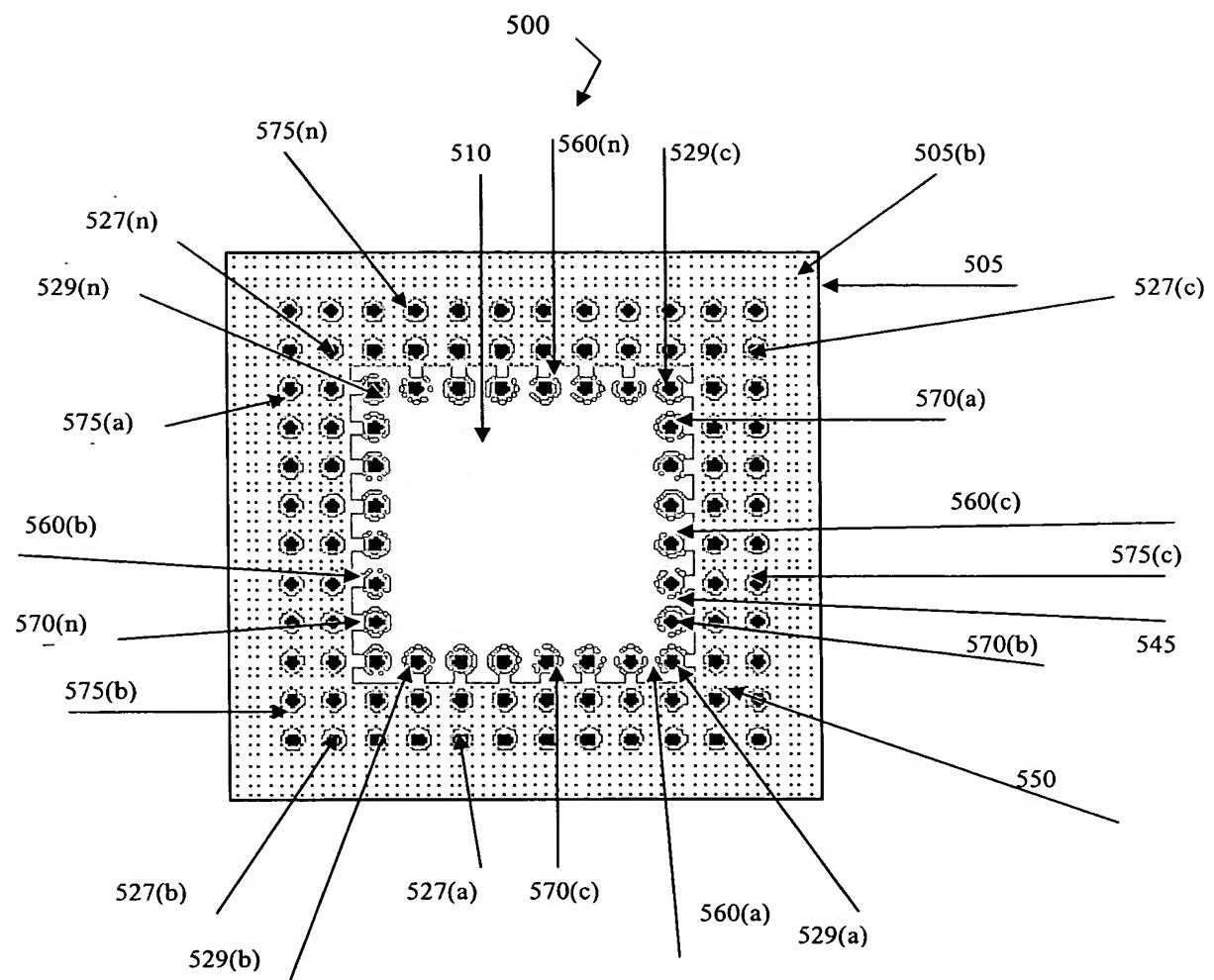


Fig. 5B

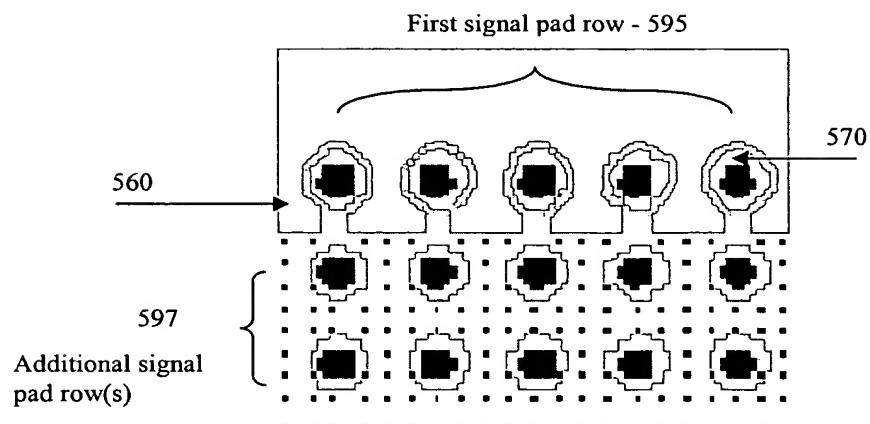


Fig. 5C

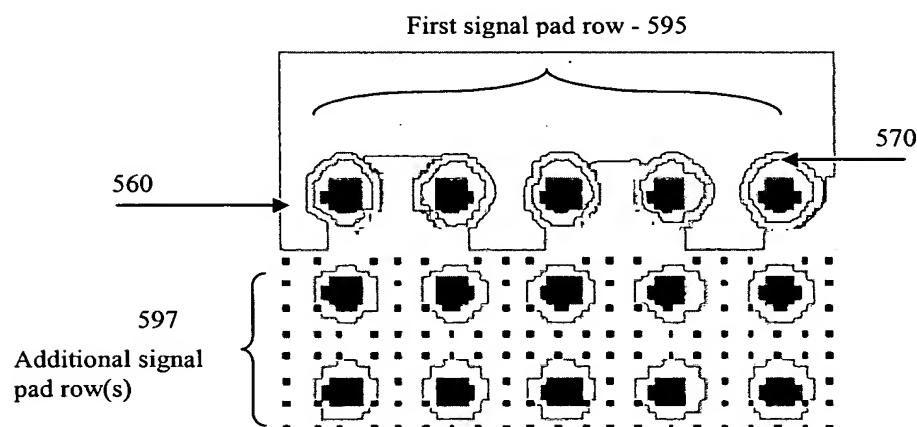


Fig. 6

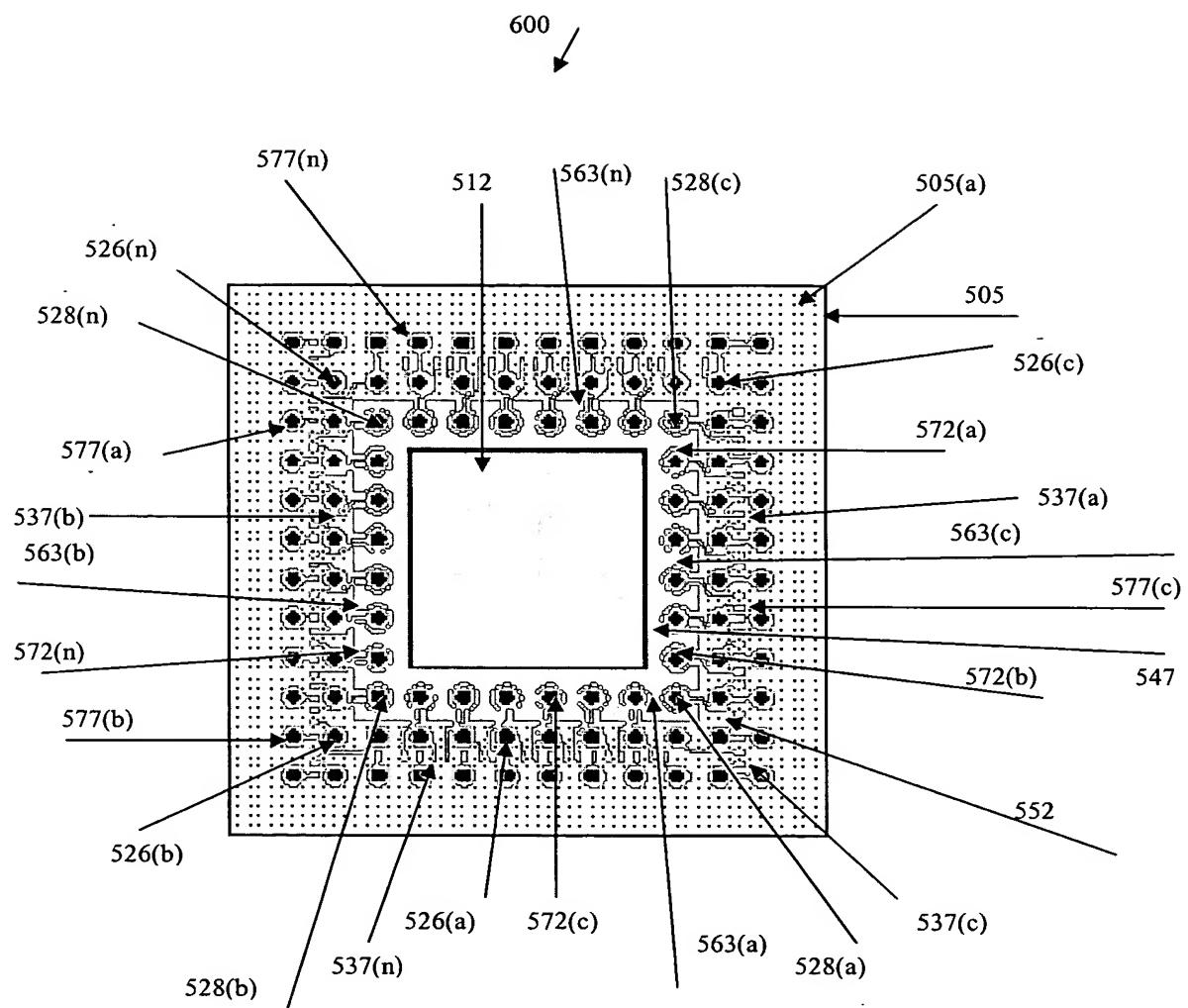


Fig. 7

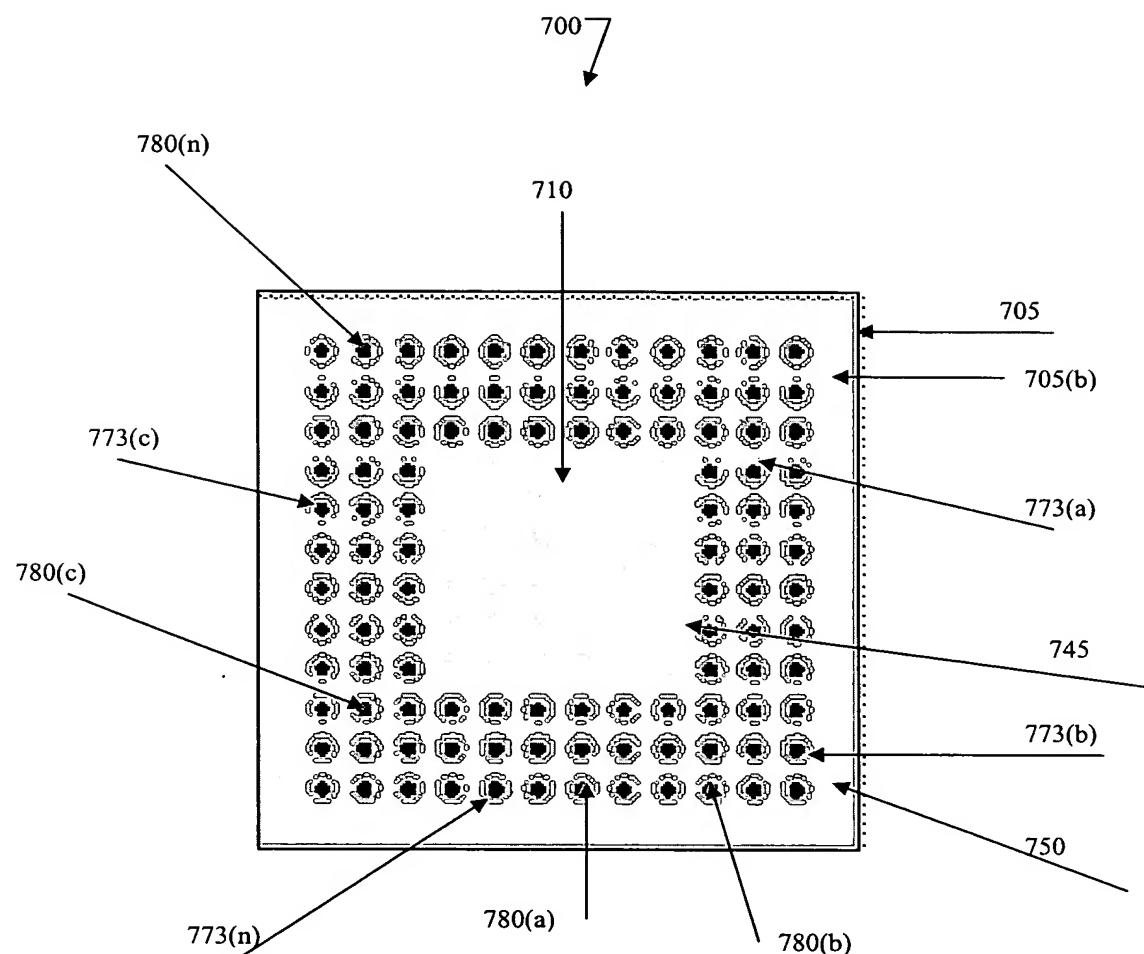


Fig. 8

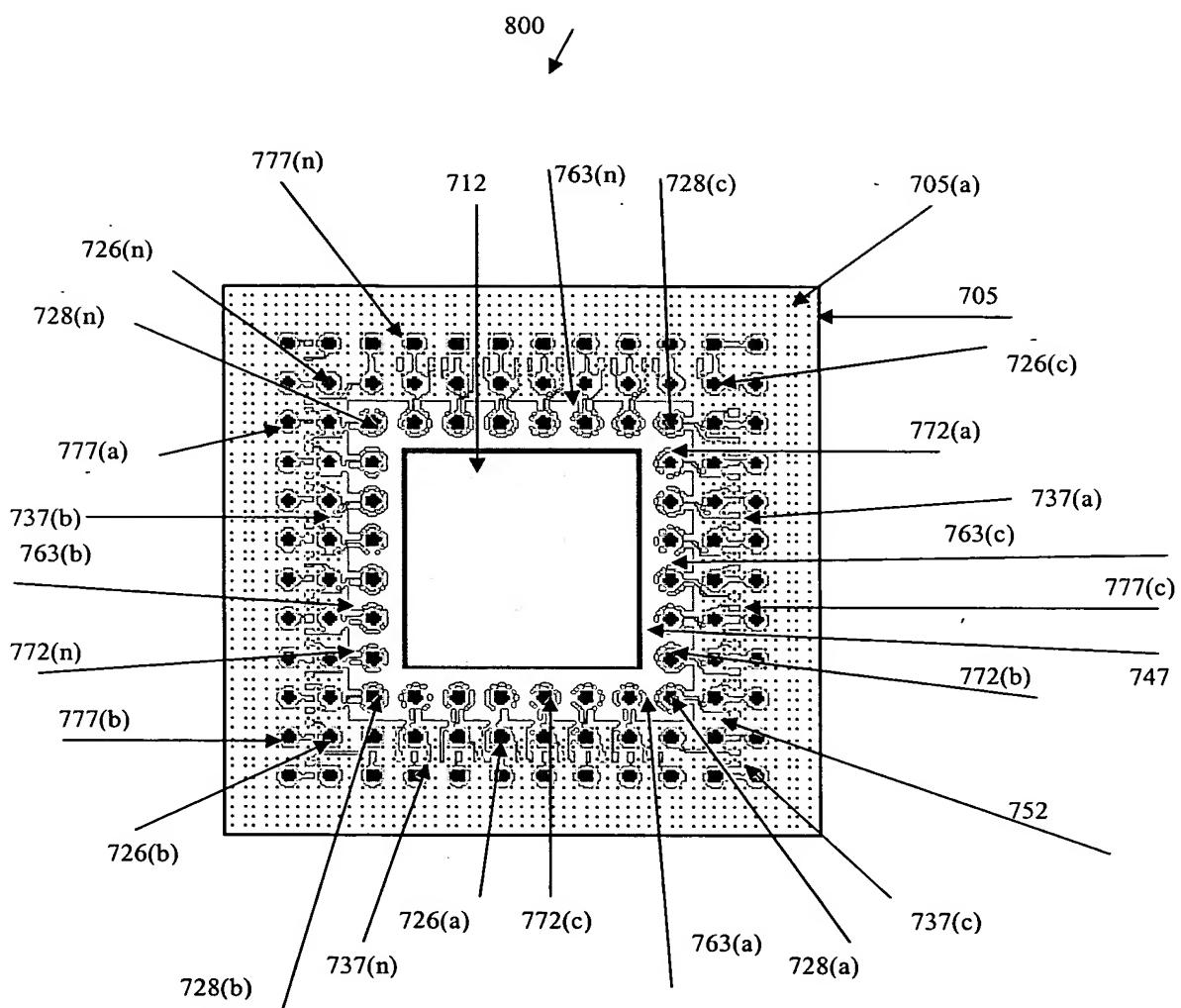
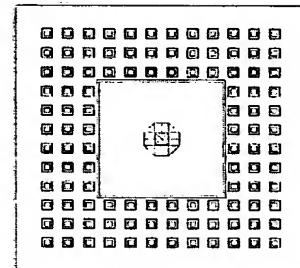
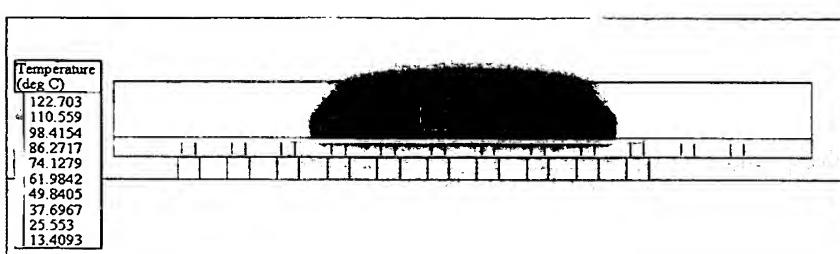
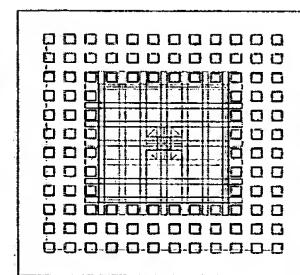
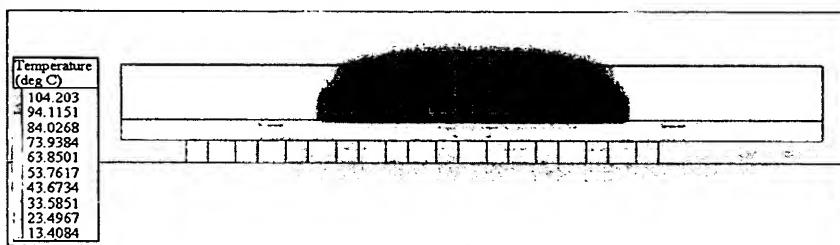


Fig. 9

1. Reference, normal design:



2. Enhanced design, top and bottom thermal fingers:

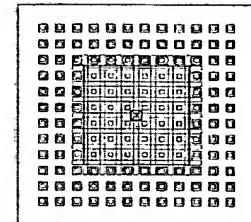
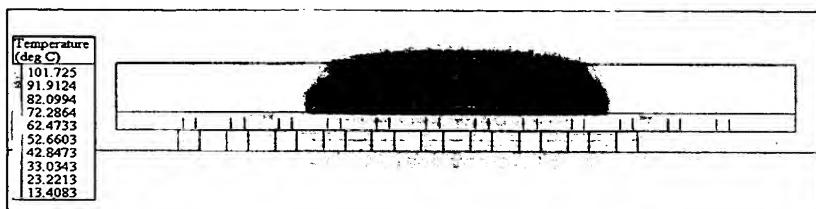


~ 15 % thermal performance decrease in package level

Fig. 10

3. Enhanced design, top and bottom thermal fingers + thermal vias:

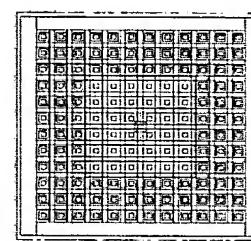
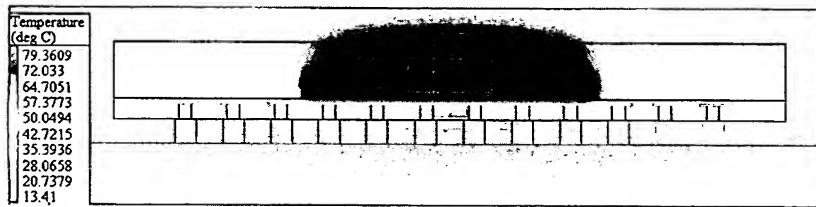
1030



~ 17 % thermal performance decrease in package level

4. Enhanced design, top thermal fingers and bottom net + vias:

1040



~ 35 % thermal performance decrease in package level